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The Following Section consists of Multiple Choice Questions on Combinational Logic Circuits. Take the Quiz and improve your overall Engineering.

**Multiple Choice
Questions on
Combinational Logic
Circuits ...**

This set of Digital
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Electronics/Circuits
Multiple Choice
Questions & Answers
(MCQs) focuses on

“Combinational
Circuits”. 1. Which of
the circuits in figure (a
to d) is the sum-of-
products
implementation of
figure (e)? a) a b) b c) c
d) d View Answer

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and ...**

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Multiple choice and true or false type questions are also provided. How to solve Digital Electronics Combinational Logic Circuits problems? You can easily solve all kind of Digital Electronics questions based on Combinational Logic Circuits by practicing the objective type exercises given below, also get shortcut methods to solve Digital

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Answers**

Combinational And
Sequential Circuits 20
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updated: Feb 13, 2013
| Total Attempts: 795
Questions All questions
5 questions 6
questions 7 questions
8 questions 9
questions 10 questions
11 questions 12

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questions 13 questions
14 questions 15
questions 16 questions
17 questions 18
questions 19 questions
20 questions

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quiz give you a good
mix of easy questions
and tough questions.

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20 Questions MCQ ...**

In a look-ahead carry
generator, the carry
generate function G_i
and the carry
propagate function P_i

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for inputs A_i and B_i
are given by: $P_i = A_i$

$\oplus B_i$ and $G_i = A_i B_i$

The expressions for the
sum bit S_i and the
carry bit C_{i+1} of the
look-ahead carry adder
are given by: $S_i = P_i$
 $\oplus C_i$ and $C_{i+1} = G_i +$
 $P_i C_i$, where C_0 is the
input carry. Consider a
two-level logic
implementation ...

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Combinational circuit is a circuit in which we combine the different gates in the circuit, for example encoder, decoder, multiplexer and demultiplexer.

Some of the characteristics of combinational circuits are following – The output of combinational circuit at any instant of time, depends only on the levels present at input terminals.

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combinational digital
circuit built from logic
gates. Full Adder

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Sequential Logic
Circuits - MCQs with
answers Q1. Which
sequential circuits
generate the feedback
path due to the cross-

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coupled connection
from output of one
gate to the input of
another gate? a.

Synchronous b.

Asynchronous c. Both

d. None of the above

View Answer / Hide

Answer

Sequential Logic Circuits - MCQs with answers

Multiple Choice

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on Digital Electronics.

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Illustrated Formulae &
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**Multiple Choice
Questions and**

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The following points
will be considered on
the quiz:

Combinational circuit
that receives a single
data input and
processes it to multiple
outputs Logic circuit
that needs to be
incorporated in its ...

**Quiz & Worksheet -
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Combinational Circuits (DC-IV) MCQs & Numerical Problems
The problems considered here are put under the following five subtopics. These are: (a) Arithmetic circuits ... The circuit is a code converter i.e. converts input bit code to some other code. It uses EX-OR gates, and the inputs to EX-OR are from the input bit and output bit. ...

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**Combinational
Circuits (DC-IV)
MCQs & Numerical
Problems**

I. Answer the following
Multiple Choice

Questions: (1) The
number . 102 . written
in binary is (a)

0110011 (b) 110011

(c) 110110 (d)

1100110 (2) In binary
arithmetic, calculate .

... Design a 4-bit
combinational circuit
decremented using
four full adders. (2)

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Differentiate between virtual and cache memory. (3) Design a 16-bit Multiplexer and

...

003-007102

Short Questions 1. Define Gates. 2. List out the basic gates. 3. Define AND Gate. 4. What do you mean by logic circuit? 5. How to obtain dual of a given expression. 6. What do you mean by truth table? 7. Define

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variable. 8. What is
Redundancy Law? 9.
Define complement.
10. List out the
Postulates. 11. What is
the method of perfect
induction? 12 ...

**Department of
Computer Science &
Technology**

Multiple choice
questions - Circle the
correct statements 1)
For the Verilog code
segment below, circle
correct answers: (a)

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Line 3 implements a shift register. (b) Line 4 implements a shift register. (c) Line 5 implements a parallel flip-flops. (d) Line 6 implements a shift register.

CSE 140L Final Exam - University of California, San Diego

By answering the multiple-choice questions on these assessments, you can find out how much you

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know about truth
tables, combinational
circuits and Boolean
algebra expressions.
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...

Digital logic design
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questions and answers
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